

**- - REMARKS - -**

These remarks are in response to a Non-Final Office Action dated September 20, 2002. Claims 1-8 are currently pending in the present application. Claims 1-8 have been amended herein to more particularly point out and distinctly claim the present invention in accordance with 35 U.S.C. §112, ¶2. No new matter was introduced by amendment to the claims.

In the Non-Final Office Action, Examiner Piziali objected to the specification for failing to include a proper abstract. To obviate this objection and to correct errors/omissions in the specification, the Applicant is concurrently filing a marked-up specification and a substitute specification in accordance with 37 CFR §1.125. No new matter was introduced into the substitute specification.

Also in the Non-Final Office Action, Examiner Piziali rejected pending claims 1-8 on various grounds. The Applicant responds to each rejection as subsequently recited herein, and respectfully requests reconsideration and further examination of the present application under 37 CFR § 1.112:

- A. Claims 1-7 were rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,677,705 to *Shimura* et al.

The Applicant has thoroughly considered Examiner Piziali's remarks concerning the patentability of independent claim 1 over *Shimura*. The Applicant has also thoroughly read *Shimura*. To warrant this §102(b) rejection of independent claim 1, *Shimura* must show each and every limitation of independent claim 1 in as complete detail as is contained in independent claim 1. See, MPEP 2131. The Applicant respectfully traverses this 35 U.S.C. §102(b) rejection of independent claim 1, because *Shimura* fails to disclose and furthermore teaches away from "wherein the mutually orthogonal signals are obtained from at least two types of orthogonal functions having

four elementary units of time, within which four elementary units of time one pulse each time has a polarity that is different from that of the other pulses” as originally recited in independent claim 1.

Specifically, *Shimura* teaches a function  $f(i,t)$  is expressed by an equation  $f(i,t) = F_p * \delta(i,t)$ , where  $\delta(i,t)=1$  when  $i=t$ , and where  $\delta(i,t)=0$  when  $i \neq t$ . See, *Shimura* at column 1, lines 56-66. An implementation of the aforementioned equation is illustrated in FIG. 3 of *Shimura*, and represented in the following TABLE 1:

TABLE 1

	t=1	t=2	t=3	t=i	t=N
f(1)	$+F_p$	0	0	0	0
f(2)	0	$+F_p$	0	0	0
f(3)	0	0	$+F_p$	0	0
f(i)	0	0	0	$+F_p$	0
f(N)	0	0	0	0	$+F_p$

The opposing polarity of  $+F_p$  is  $-F_p$ . TABLE 1 and FIG. 3 of *Shimura* clearly point out the failure of *Shimura* to disclose a polarity  $+F_p$  of function  $f(1)$  being different than the polarities of the other functions when  $t=1$  (i.e., the other functions having a  $-F_p$  polarity), a polarity  $+F_p$  of function  $f(2)$  being different than the polarities of the other functions when  $t=2$ , a polarity  $+F_p$  of function  $f(3)$  being different than the polarities of the other functions when  $t=3$ , a polarity  $+F_p$  of function  $f(i)$  being different than the polarities of the other functions when  $t=i$ , and a polarity  $+F_p$  of function  $f(N)$  being different than the polarities of the other functions when  $t=N$ .

Furthermore, each function is associated with a voltage  $U(i,j)$ . FIG. 4 of *Shimura* illustrates the objective of one function being  $+F_p$  while the other functions are 0 for each time  $t$  is to ensure that, for each time  $t$ , only one voltage  $U(i,j)$  equals a high voltage  $S1$  while the other voltages  $U(i,j)$  are between low voltages  $S2$  and  $S3$ . The Applicant therefore respectfully asserts that *Shimura* teaches away from one voltage  $U(i,j)$  equaling a high voltage  $S1$  while the remaining voltage  $U(i,j)$  equal  $-S1$  for each time  $t$ . As such, *Shimura* also teaches away from one function being  $+F_p$  while the other functions are  $-F_p$  for each time  $t$ .

To more particularly point out and distinctly claim the present invention over *Shimura*, the Applicant has amended claim 1 to recite “wherein the mutually orthogonal signals are obtained from at least two types of orthogonal functions having four elementary units of time, within which four elementary units of time one pulse each time has a first polarity which opposes a second polarity of the other pulses”

Withdrawal of the rejection of independent claim 1 under 35 U.S.C. §102(b) as being anticipated by *Shimura* is therefore respectfully requested.

Claims 2-7 depend from independent claim 1. Therefore, dependent claims 2-7 include all of the elements and limitations of independent claim 1. It is therefore respectfully submitted by the Applicant that dependent claims 2-7 are allowable over *Shimura* for at least the same reason as set forth with respect to independent claim 1. Withdrawal of the rejection of dependent claims 2-7 under 35 U.S.C. §102(b) as being anticipated by *Shimura* is therefore respectfully requested.

The Applicant has added herein an independent claim 9 and claims 10-16 depending from independent claim 9, which recites “wherein, for each elementary unit of time, one pulse has a first polarity that opposes a second polarity of the other pulses”. It is therefore respectfully submitted by the Applicant that claims 9-16 are allowable over *Shimura* and the other art of record for at least the same reason as set forth herein with respect to independent claim 1. An allowance of claims 9-16 is therefore respectfully requested.

- B.** Claim 8 was rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,677,705 to *Shimura* et al. in view of U.S. Patent No. 6,252,573 B1 to *Ito* et al.

Claim 8 depends from independent claim 1. Therefore, dependent claim 8 includes all of the elements and limitations of independent claim 1. It is therefore respectfully submitted by the Applicant that dependent claim 8 is allowable over *Shimura* in view of *Ito* for at least the same reason as set forth with respect to independent claim 1. Withdrawal of the rejection of dependent claim 8 under 35 U.S.C. §103(a) as being unpatentable over *Shimura* in view of *Ito* is therefore respectfully requested.

**SUMMARY**

Examiner Piziali's objection to the specification has been obviated by the concurrent submission of a responsive substitute specification. Examiner Piziali's 35 U.S.C. §§102(b) and 103(a) rejections of claims 1-9 have been obviated by the remarks herein concerning the patentability of independent claim 1 over *Shimura*. The Applicant respectfully submits that claims 1-16 herein fully satisfy the requirements of 35 U.S.C. §§ 102, 103 and 112. In view of the foregoing, favorable consideration and early passage to issue of the present application is respectfully requested.

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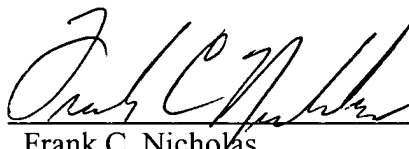
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**MARKED-UP VERSION**  
**OF AMENDED CLAIMS TO SHOW CHANGES MADE:**

1. (AMENDED) A display device comprising a liquid crystal between a first substrate provided with row or selection electrodes and a second substrate provided with column or data electrodes, in which overlapping parts of row and column electrodes define pixels, drive means for driving the column electrodes in conformity with an image to be displayed, and drive means for driving the row electrodes which, in the operating condition, sequentially supply groups of  $p$  row electrodes with  $p$  mutually orthogonal signals, [characterized in that] wherein the mutually orthogonal signals are obtained from at least two types of orthogonal functions having four elementary units of time, within which four elementary units of time one pulse [time] each time has a first polarity which [is different from that] opposes a second polarity of the other pulses.

2. (AMENDED) [A] The display device [as claimed in] of claim 1, [characterized in that] wherein the orthogonal signals are obtained from orthogonal functions having four elementary units of time, within which four elementary units of time the one pulse having [a] the first polarity which [differs from that] opposes the second polarity of the other pulses each time shifts by one elementary unit of time.

3. (AMENDED) [A] The display device [as claimed in] of claim 1 or 2, [characterized in that] wherein the orthogonal signals are obtained from orthogonal functions having four elementary units of time which, viewed in a time sequence, are situated one after the other.

4. (AMENDED) [A] The display device [as claimed in] of claim 3, [characterized in that] wherein at least two orthogonal signals have opposed DC contents.

5. (AMENDED) [A] The display device [as claimed in] of claim 1 or 2, [characterized in that] wherein the orthogonal signals are obtained from orthogonal functions having four elementary units of time, in which the elementary units of the orthogonal functions are interwoven.

6. (AMENDED) [A] The display device [as claimed in] of claim 1 or 2, [characterized in that] wherein  $p = 4$ , and in that four orthogonal signals have identical DC contents and four are free from a DC voltage.

7. (AMENDED) [A] The display device [as claimed in] of claim 6, [characterized in that] wherein the DC content of 2 orthogonal signals of the orthogonal signals having an identical DC content is opposed to that of the two other orthogonal signals.

8. (AMENDED) [A] The display device [as claimed in] of claim 1 or 2, [characterized in that] wherein the drive means [invert] inverts the orthogonal signals after each frame period.

[Display device]

## DISPLAY DEVICE

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## BACKGROUND OF THE INVENTION

### 1. Field of the Invention

The invention relates to a display device [comprising] employing a liquid crystal material between a first substrate provided with row or selection electrodes and a second  
10 substrate provided with column or data electrodes, in which overlapping parts of the row and column electrodes define pixels[.]. The display device further employs [drive means] drivers for driving the column electrodes in conformity with an image to be displayed, and [drive means] for driving the row electrodes which, in the operating condition, sequentially supply groups of  $p$  row electrodes with  $p$  mutually orthogonal signals. Such display devices  
15 are used in, for example, portable apparatuses such as laptop computers, notebook computers and telephones.

### 2. Description of the Related Art

Passive-matrix displays of this type are generally known and, for realizing a high  
20 number of lines, they are increasingly based on the STN (Super-Twisted Nematic) effect. An article by T.J. Scheffer and B. Clifton "Active Addressing Method for High- Contrast Video Rate STN Displays", SID Digest 92, pp. 228-231 describes how the phenomenon of "frame response" which occurs with rapidly switching liquid crystal materials is avoided by making use of "Active Addressing". In this method, all rows are driven throughout the frame period  
25 with mutually orthogonal signals, for example, Walsh functions. The result is that each pixel is continuously excited by pulses (in an STN LCD of 240 rows: 256 times per frame period) instead of once per frame period. In "multiple row addressing", a (sub-)group of  $p$  rows is driven with mutually orthogonal signals. Since a set of orthogonal signals, such as Walsh functions, consists of a plurality of functions which is a power of 2, i.e.  $2^S$ ,  $p$  is preferably  
30 chosen to be equal thereto as much as possible, i.e. generally  $p = 2^S$  (or also  $p = 2^S - 1$ ). The orthogonal row signals  $F_i(t)$  are preferably square-wave shaped and consist of voltages  $+F$  and  $-F$ , while the row voltage is equal to zero outside the selection period. The elementary voltage pulses from which the orthogonal signals are built up are regularly distributed across



the frame period. In this way, the pixels are then excited  $2^S$  (or  $(2^S-1)$ ) times per frame period with regular intermissions instead of once per frame period. Even for low values of  $p$  such as  $p = 3$  (or 4) or  $p = 7$  (or 8) the frame response appears to be suppressed just as satisfactorily as when driving all rows simultaneously, such as in "Active Addressing", but it requires much less electronic hardware.

However, it appears that, notably for Walsh functions, the frequency content of the functions from a complete set of functions is greatly different. Since the dielectric constant of liquid crystalline material is frequency-dependent, this may cause the liquid crystalline material to react differently at different positions in, for example, a matrix display, dependent on the image contents. This leads to artefacts in the image such as different forms of crosstalk.

#### SUMMARY OF THE INVENTION

It is[, inter alia,] an object of the invention to provide a display device [of the type described above,] in which a minimal number of artefacts occurs in the image.

To this end, a display device according to the invention is characterized in that the mutually orthogonal signals are obtained from at least two types of orthogonal functions having four elementary units of time, within which four elementary units of time one pulse each time has a polarity which is different from that of the other pulses.

It is found that orthogonal signals can thereby be generated which differ little in frequency content and thus do not give rise or hardly give rise to artefacts in the image. Such orthogonal signals are obtained, for example, from orthogonal functions having four elementary units of time, within which four elementary units of time the pulse having a polarity which differs from that of the other pulses each time shifts by one elementary unit of time. The use of four elementary units of time has the additional advantage that the number of column voltage levels remains limited to five, while this number is six when using, for example, three elementary units of time, within which three elementary units of time one pulse having a polarity which differs from that of the other pulses shifts by only one unit of time. A larger number of column voltage levels to be used of course leads to more expensive drive electronics.

These and other aspects of the invention are apparent from and will be elucidated with reference to the embodiments described hereinafter.

### BRIEF DESCRIPTION OF THE DRAWINGS

[In the drawings:]

Fig. 1 shows diagrammatically a display device in which the invention is used, and

5 Figs. 2 and 3 show sets of 4 and 8 Walsh functions, respectively, and orthogonal signals derived therefrom for the purpose of multiple row addressing, while

Fig. 4 shows another set of four orthogonal functions according to the invention, and orthogonal signals derived therefrom for the purpose of multiple row addressing, and

Fig. 5 shows a generalization of Fig. 4, while

10 Figs. 6 and 7 show some orthogonal signals according to the invention, derived from Fig. 5, for the purpose of multiple row addressing[.], and

Fig. 8. shows mixed orthogonal signals according to the invention, derived from Fig. 5, for the purpose of multiple row addressing.

### DETAILED DESCRIPTION OF THE PRESENTLY PREFERRED EMBODIMENTS

15 Fig. 1 shows a display device comprising a matrix 1 of pixels at the area of crossings of  $N$  rows 2 and  $M$  columns 3 which are provided as row and column electrodes on facing surfaces of substrates 4, 5, as can be seen in the cross-section shown in the matrix 1. The liquid crystal material 6 is present between the substrates. Other elements such as orientation  
20 layers, polarizers, etc. are omitted for the sake of simplicity in the cross-section.

The device further comprises a row function generator 7 in the form of, for example, a ROM for generating orthogonal signals  $F_i(t)$  for driving the rows 2. Similarly as described in said article by Scheffer and Clifton, row vectors driving a group of  $p$  rows via drive  
25 circuits 8 are defined during each elementary time interval. The row vectors are written into a row function register 9.

Information 10 to be displayed is stored in a  $pxM$  buffer memory 11 and read as information vectors per elementary unit of time. Signals for the column electrodes 3 are obtained by multiplying the then valid values of the row vector and the information vector during each elementary unit of time and by subsequently adding the  $p$  obtained products.

30 The multiplication of the values which are valid during an elementary unit of time of the row and column vectors is realized by comparing them in an array 12 of  $M$  exclusive ORs. The addition of the products is effected by applying the outputs of the array of exclusive ORs to the summing logic 13. The signals 16 from the summing logic 13 drive a column drive

circuit 14 which provides the columns 3 with voltages  $G_j(t)$  having  $p+1$  possible voltage levels. Every time,  $p$  rows are driven simultaneously, in which  $p < N$  ("multiple row addressing"). The row vectors therefore only have  $p$  elements, as well as the information  
 5 vectors, which results in a saving of the required hardware such as the number of exclusive ORs and the size of the summing circuit, as compared with the method in which all rows are driven simultaneously with mutually orthogonal signals ("Active Addressing").

[As stated in the opening paragraph, it] It is possible to use less drive electronics by choosing  $p$  to be low, for example, in the range between 3 and 8. Fig. 2 shows a frequently  
 10 used set of orthogonal functions referred to as Walsh functions (Fig. 2a) and the pulse patterns derived therefrom for the purpose of multiple row addressing (Fig. 2b), with  $p = 4$ . It is clear that the frequency content of the lumped functions, or the number of sign changes within the derived pulse patterns, greatly differs for each one of the different functions. The first function (1) comprises DC components, because the lumped function consists of half a  
 15 period of a square wave, whereas the other functions do not comprise any DC component. The second function (2) comprises, within one period, a (square) wave with the double frequency of the first function. The fourth function (4) is doubled in frequency again with respect to the second function, while the third function (3) is a shifted variant of the fourth function. Even when the first function is not used to avoid DC effects, there is a great  
 20 difference in frequency content of the three remaining functions. The dielectric constant of the liquid crystal material is frequency-dependent so that, dependent on the image contents, the use of such functions may lead to artefacts such as crosstalk. The same applies when using Walsh functions (Fig. 3a) and the pulse patterns derived therefrom for the purpose of multiple row addressing (Fig. 3b), with  $p = 8$ .

25 Fig. 4 shows another set of four orthogonal functions (Fig. 4a) and the pulse patterns derived therefrom for the purpose of multiple row addressing (Fig. 4b), with  $p = 4$ . The frequency content of the lumped functions, or the number of sign changes within the pulse patterns derived therefrom is now substantially the same for each one of the different functions. This set is obtained by shifting the negative pulse each time by one position in the  
 30 second and subsequent functions. Since such a set, in which the sign-different pulse is each time shifted by one position, is very attractive, this function is shown in a generalized form in Fig. 5 for  $p$  pulses consisting of one negative pulse and  $(p-1)$  positive pulses, with the negative pulse being shifted each time by one position in the second and subsequent

functions. The positive pulses have an amplitude  $A_p$  and the negative pulses have an amplitude  $A_n$ . To be mutually orthogonal, it holds for the two functions that their product, summed over a period of the duration of the set must be zero, or:

$$-2 A_n \cdot A_p + (p-2) \cdot A_p^2 = 0 ; \quad \text{which yields } A_n = A_p \cdot (p-2)/2 \quad (1)$$

In addition, the effective value of the function must be 1 (normalized for the function  $F$ ). This leads to

$$\frac{A_n^2 + (p-1)A_p^2}{p} = 1 \quad (2)$$

It follows from (1) and (2) for  $A_p$  and  $A_n$  that  $A_p = 2/\sqrt{p}$  and  $A_n = \frac{p-2}{\sqrt{p}}$ , respectively.

- 10 For  $p = 4$  it holds that  $A_p = A_n = 1$  and the number of possible column voltages is 5. This is higher for other values; for  $p = 3$ , the number of possible column voltages is 6, namely  $(-5/2)A_p$ ,  $(-3/2)A_p$ ,  $(-1/2)A_p$ ,  $(1/2)A_p$ ,  $(3/2)A_p$  en  $(5/2)A_p$ .  
However, when using Walsh functions, the number of required column voltage levels would be 4 for  $p = 3$  (a subject chosen from a set of 4 Walsh functions).

- 15 The invention is based on the recognition that orthogonal functions are selected as starting points based on mutually orthogonal signals obtained from at least two types of orthogonal functions with four elementary units of time, as is shown in Fig. 4. Starting from the functions of Fig. 4, these are repeated, for example, after 4 elementary units of time (patterns (1), (2), (3) and (4) in Fig. 6) or inverted and repeated (patterns (5), (6), (7) and (8) in Fig. 6).  
20 Although there is still some variation of the frequency content, these functions surprisingly appear to give less rise to artefacts than the set of 8 Walsh functions, while the number of required column voltages remains the same, namely 9.

- The pulse patterns derived from (1), (2), (3) and (4) comprise a DC component. To reduce its influence, preferably 2 of these pulse patterns in a set to be chosen are inverted (the  
25 DC content is now opposed). For a completely DC-free drive, all signals from the used set are inverted after each frame period.

This set is denoted as  $K8(5,r)$  (Kuijk function) because in the fifth (5,\*) pattern, the negative pulse starts in the second half period with a negative pulse (at the fifth position) which shifts to the right (5,r) in the subsequent patterns. Fig. 7 shows the Kuijk function  $K8(7,r)$ . It holds for both Figures that the pulse patterns derived from the patterns (5), (6), (7) and (8) are DC-free. Overall, 8 of these sets can be formed in this way, namely  $K8(5,r)$ ,  $K8(6,r)$ ,  $K8(7,r)$ ,  $K8(8,r)$ ,  $K8(5,1)$ ,  $K8(6,1)$ ,  $K8(7,1)$  and  $K8(8,1)$  in which 1 indicates that the negative pulse starts in the second half period with a negative pulse (at the specified position) which shifts to the left in the subsequent patterns.

The set of  $K(uijk)$  functions can be further extended by mixing, as it were, the two types of orthogonal functions shown in Fig. 4 with four elementary units of time. Fig. 8 shows such a set  $K8(3,r)$ . The pattern (1), in Fig. 8 is obtained by inserting pattern (1) of Fig. 4a again from the third position of pattern 1 (indicated as b in Fig. 8) and by subsequently completing pattern (1). The patterns (5), (6), (7) and (8) in Fig. 8 are obtained by inserting into the patterns (1), (2), (3) and (4) of Fig. 4 in the inverted form a pattern b. In this way pattern b and pattern a are interwoven, as if were. Patterns (2), (3) and (4) are obtained by shifting a negative pulse to the right within both part b and part a ([formel] formed by the two other parts). The pulse patterns derived from the patterns (5), (6), (7) and (8) in Fig. 8 are now again DC-free. Since this insertion can take place at four positions (elementary units of time) and the negative pulse can shift to the right and to the left, the possible number of functions based on pattern (1) of Fig. 3 is multiplied by 8. Since said inversion is also possible for the functions (2), (3) and (4) of Fig. 3, the total possible number of  $K(uijk)$  functions is 840.

The invention is of course not limited to the embodiments shown. Similarly as described above, more than 2 functions of Fig. 4 can be combined to obtain drive patterns with, for example,  $p = 16$ .

The protective scope of the invention is not limited to the embodiments described. The invention resides in each and every novel characteristic feature and each and every combination of characteristic features. Reference numerals in the claims do not limit their protective scope. The use of the verb "to comprise" and its conjugations does not exclude the presence of elements other than those stated in the claims. The use of the article "a" or "an" preceding an element does not exclude the presence of a plurality of such elements.

## BSTRACT:

A device for multiple row addressing is driven with pulse patterns based on sets of 8 (or more) orthogonal functions which have a less varying frequency content than pulse patterns based on a set of 8 Walsh functions. Mutually orthogonal signals are obtained from at least two types of the orthogonal functions having four elementary units of time. Within the four elementary units of time, one pulse each unit of time has a polarity which is different from the plurality of the other pulses.

10 [Fig. 6]